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REVISION HISTORY

Date	Author	Ver	Comments
2012/08/27	Jeff/Jerry	1.00	For SR stage.
2012/09/14	Jeff/Jerry	1.01	For SR2 stage.
2012/10/20	Jeff/Jerry	1.02	For ER stage.
2012/11/07	Jeff/Jerry	1.03	For ER2 stage.
2012/11/21	Jeff/Jerry	1.03a	For PR stage.

Unihan

Title :

Contents&History

MAIN BOARD

Engineer: Jeff Y Hsiao/Jerry Lo

Size

B

Project Name

L2300

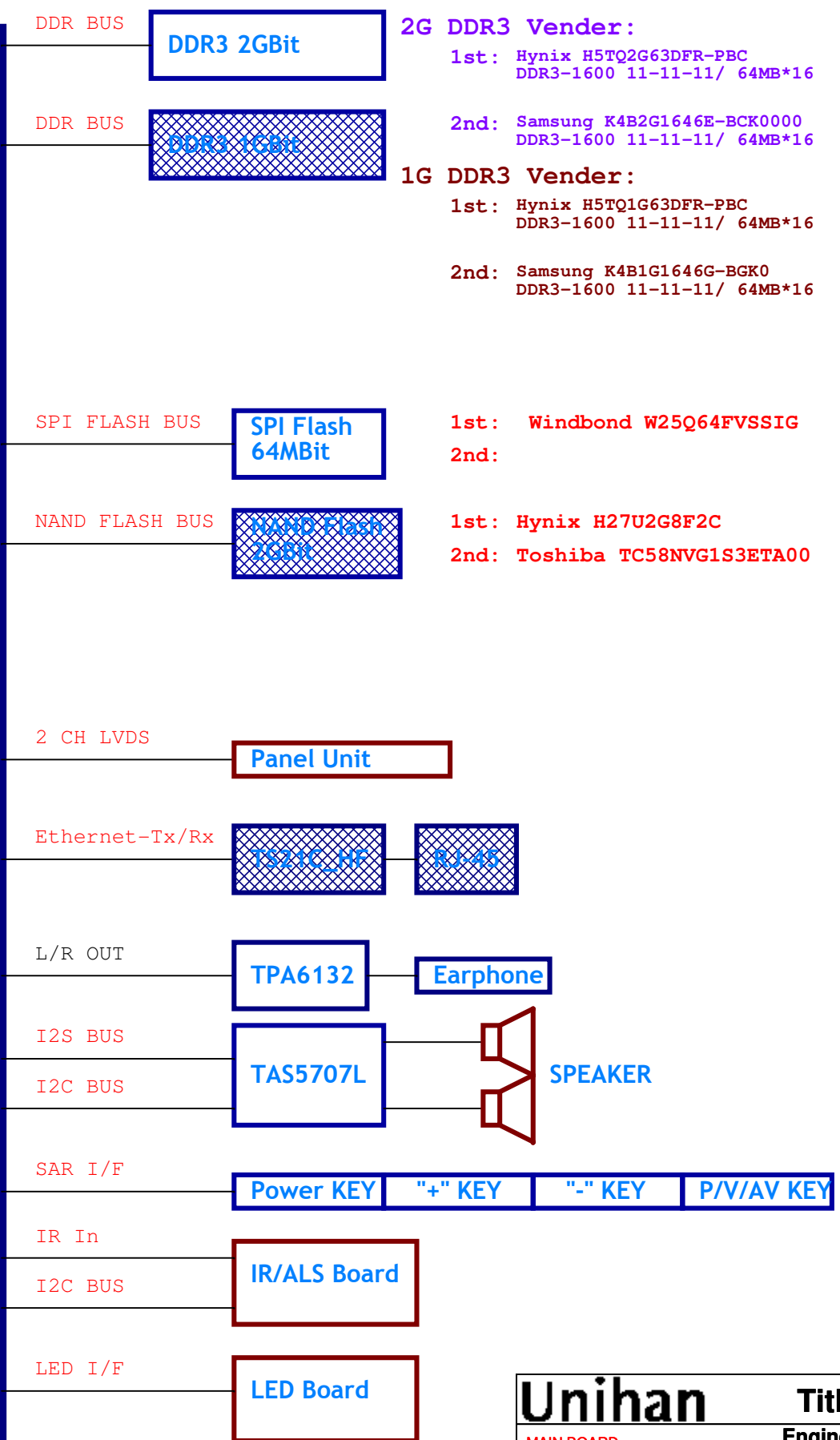
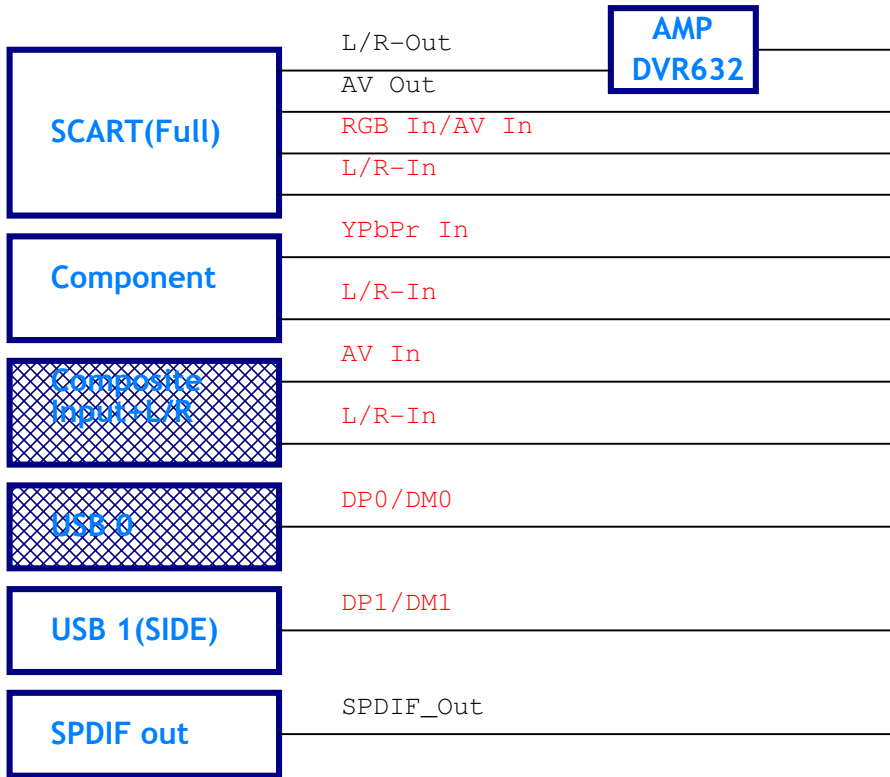
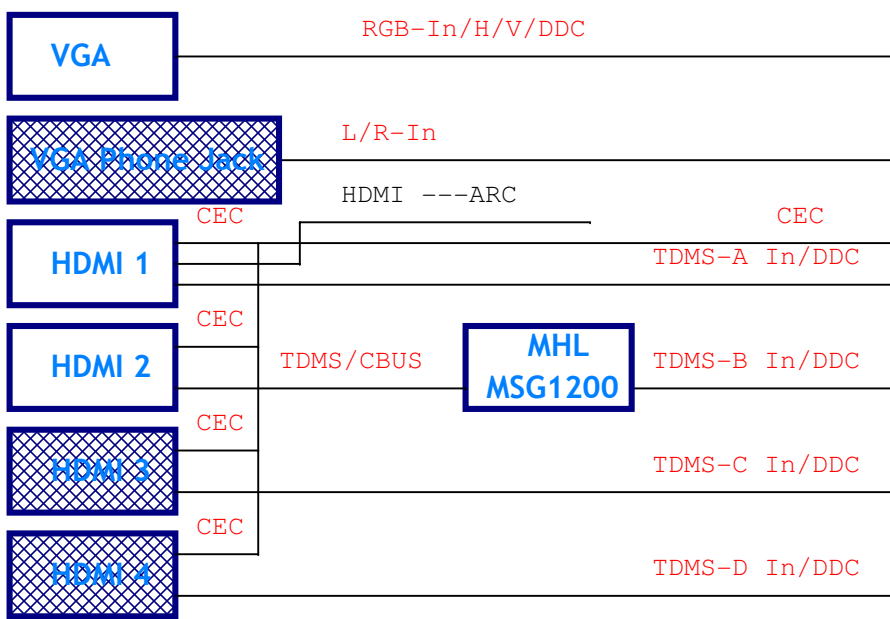
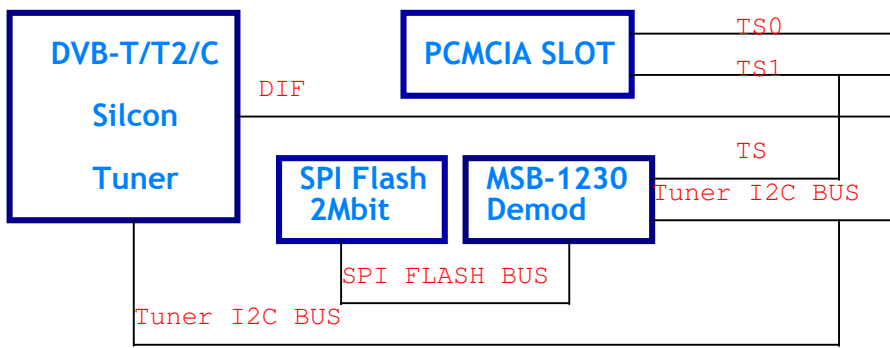
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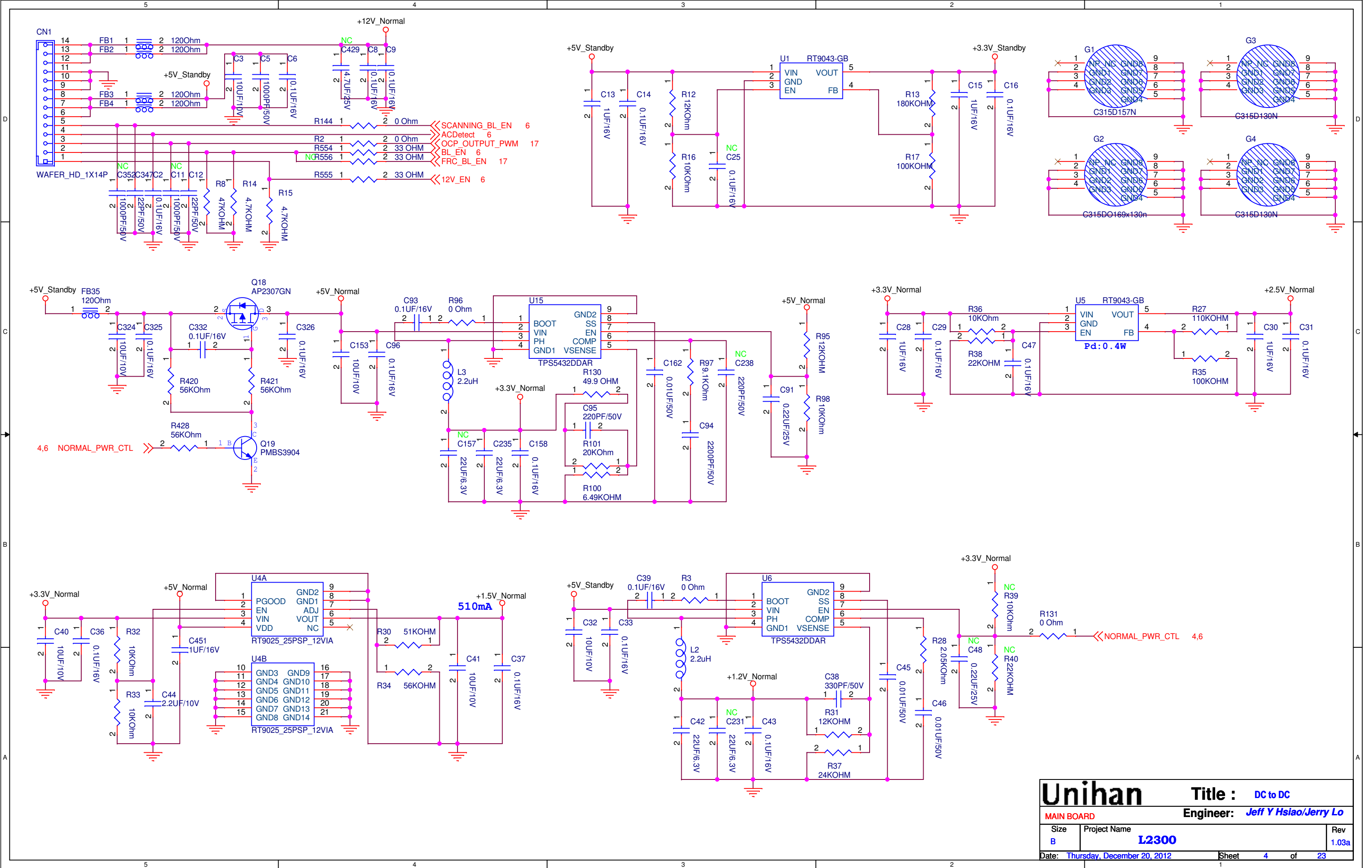
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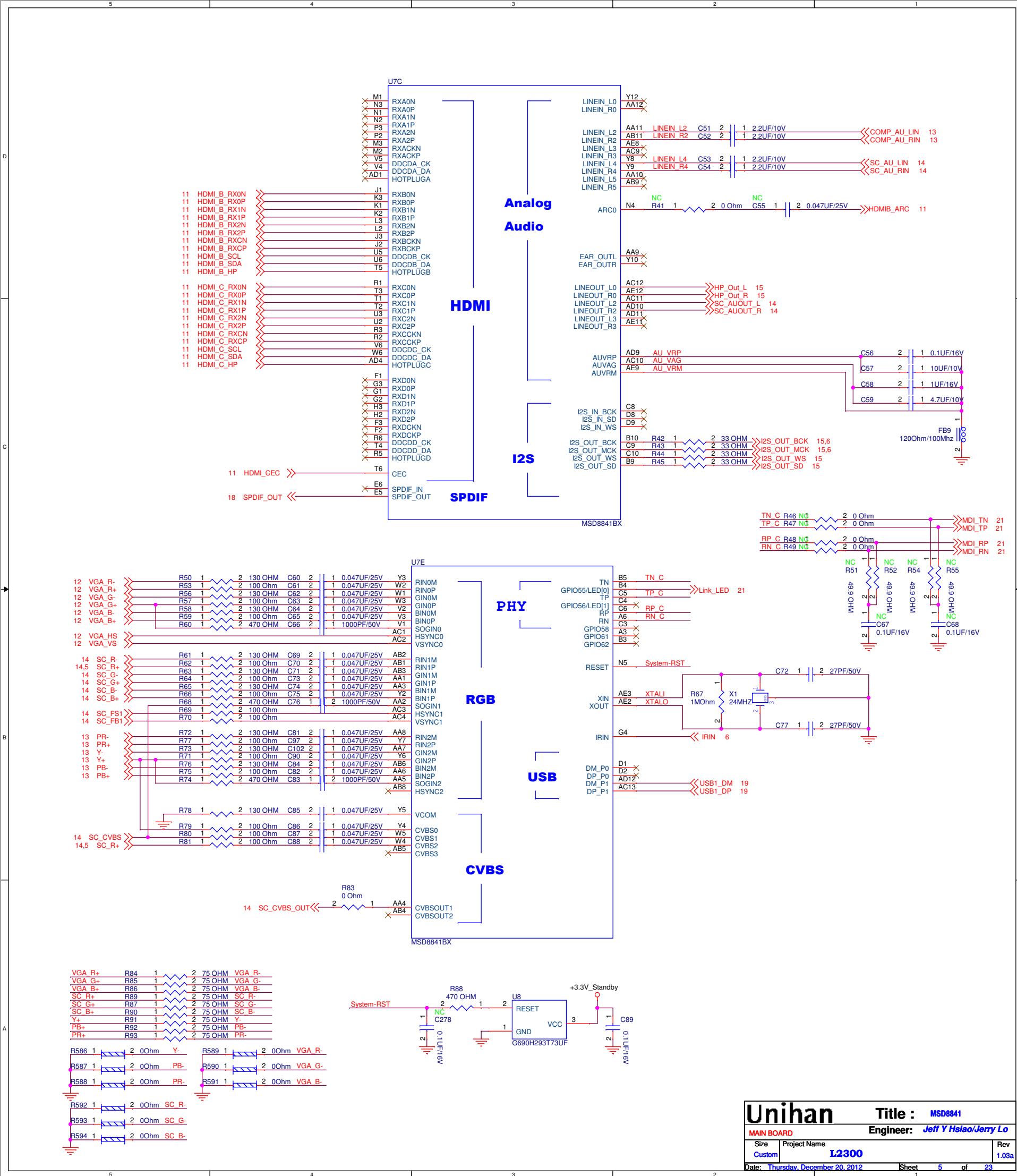
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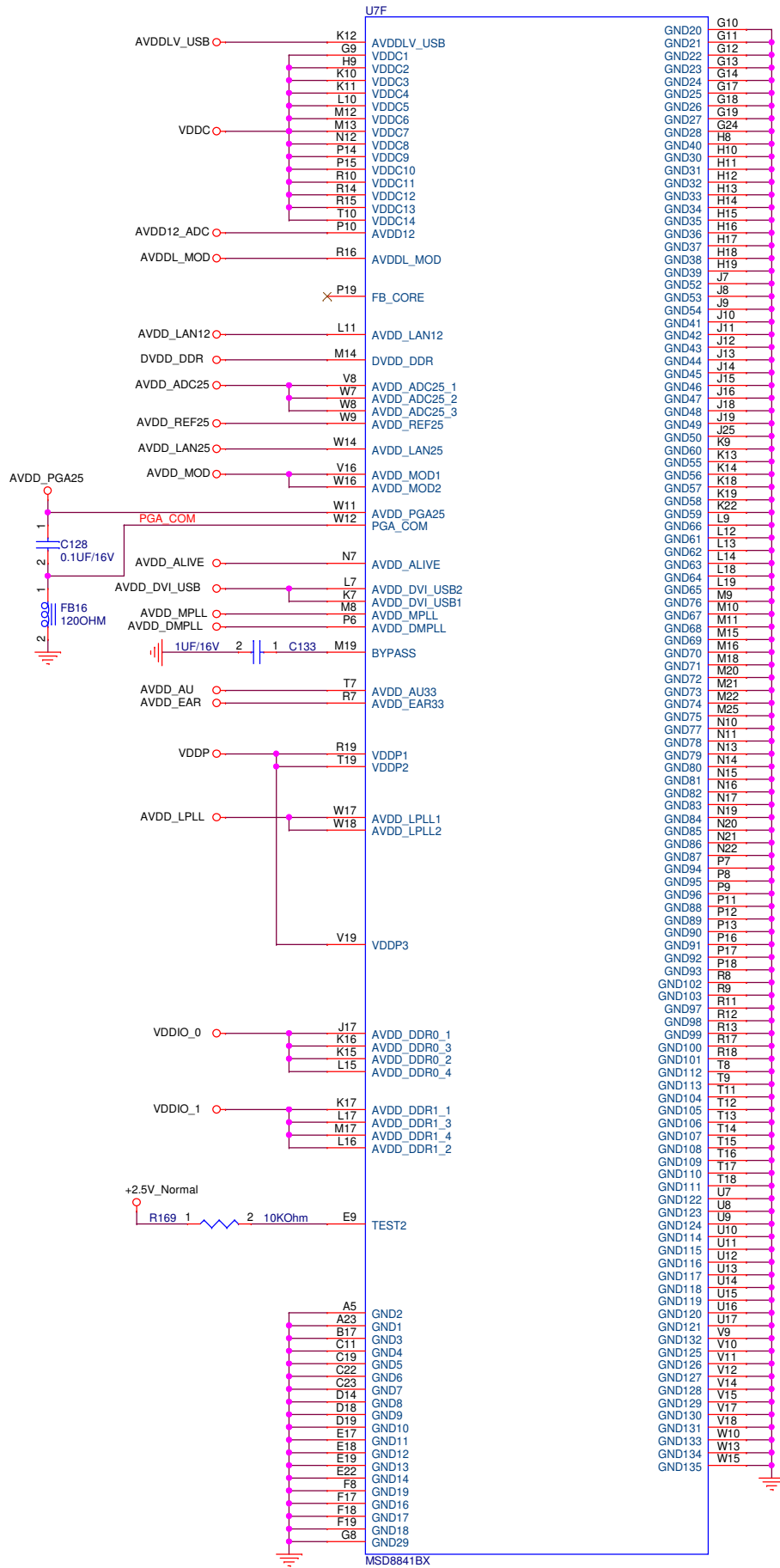
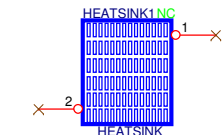
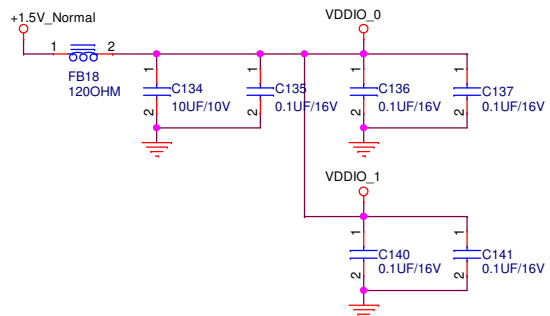
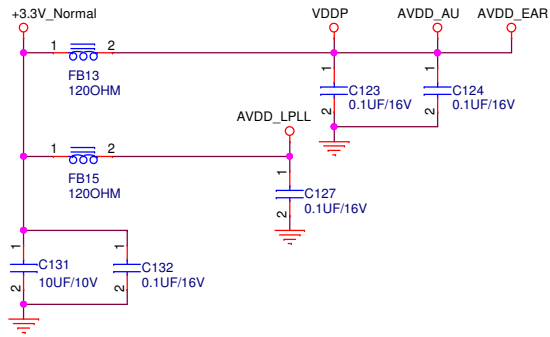
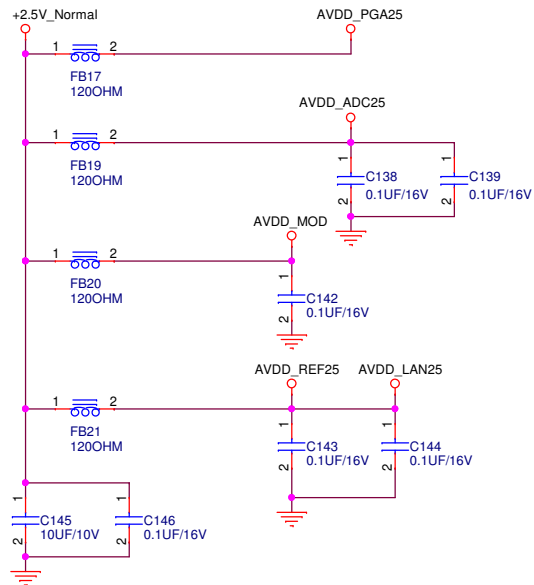
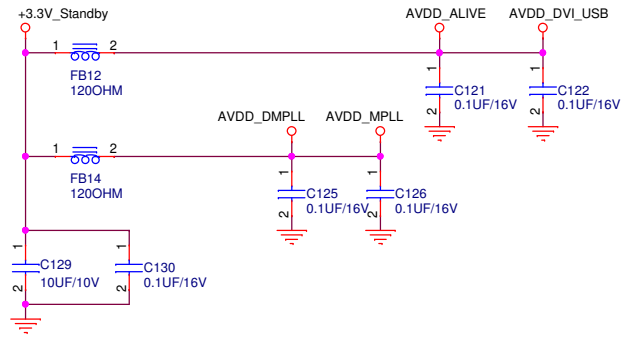
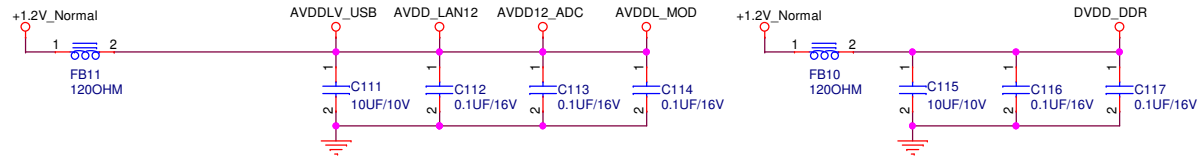
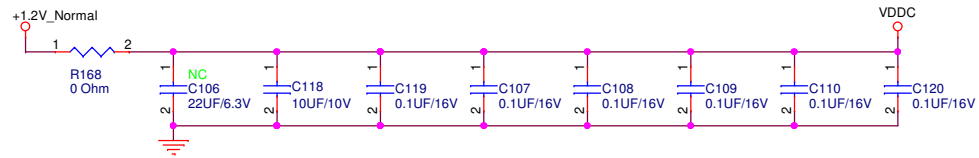
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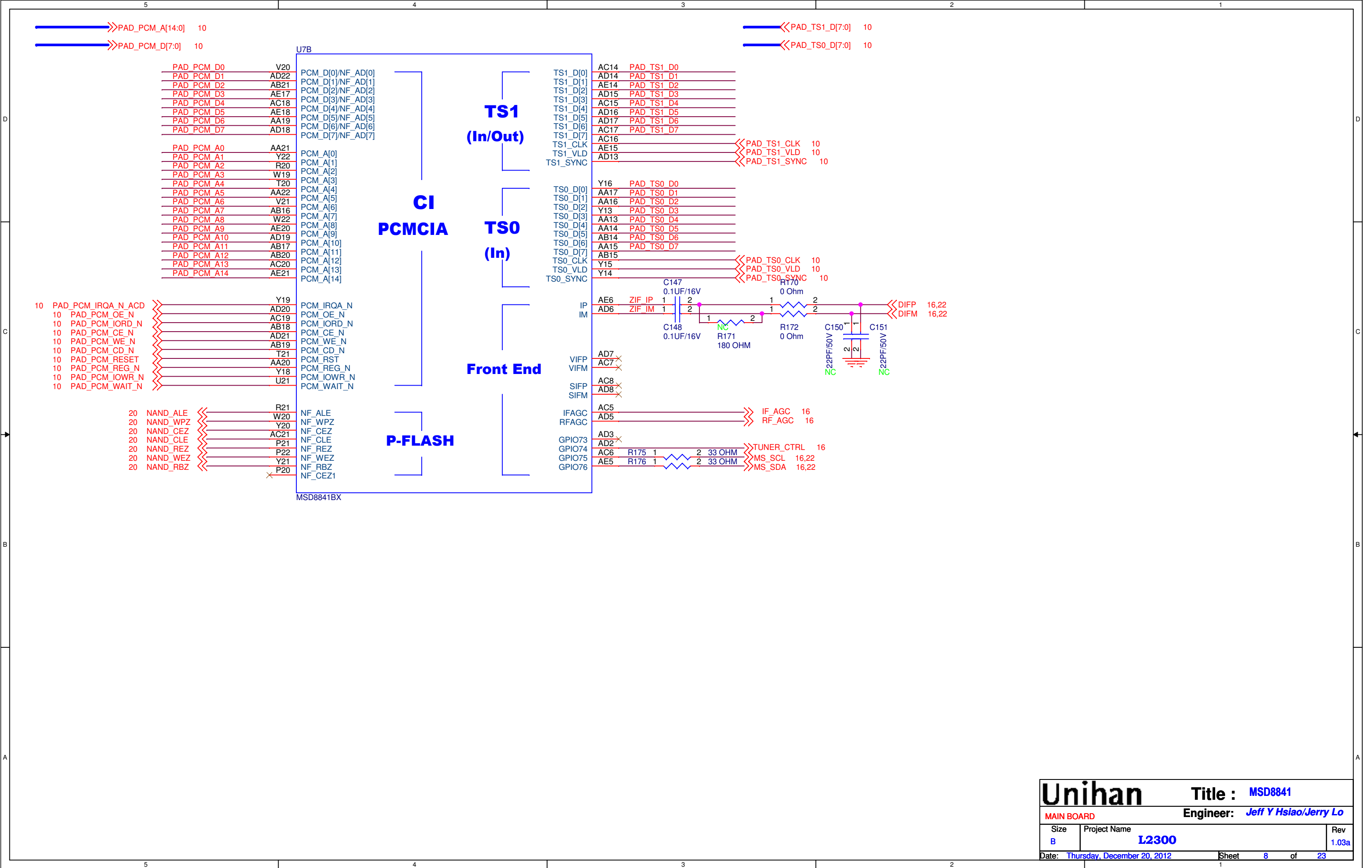
The diagram illustrates a power supply system for a 32WX/32FHD/39FHD/50FHD LED Driver. It shows the power flow from an AC input (220-240V) through a PSU Unit to various components. Key components include a DC/DC converter (U2), LDOs (U1, U3, U4, U5, U6, U7, U8, U9, U10, U11, U12, U13, U14, U15, U16, U17, U18, U19, U20, U21, U22, U23, U24, U25, U26, U27, U28, U29, U30, U31, U32, U33, U34, U35, U36, U37, U38, U39, U40, U41, U42, U43, U44, U45, U46, U47, U48, U49, U50, U51, U52, U53, U54, U55, U56, U57, U58, U59, U60, U61, U62, U63, U64, U65, U66, U67, U68, U69, U70, U71, U72, U73, U74, U75, U76, U77, U78, U79, U80, U81, U82, U83, U84, U85, U86, U87, U88, U89, U90, U91, U92, U93, U94, U95, U96, U97, U98, U99, U100), and various peripheral components like a Tuner, SPDIF Out Circuit, SC_VBS Out Circuit, MHL to HDMI, and various memory and storage components. The diagram also shows the power requirements for different display modes (32WX, 32FHD, 39FHD, 50FHD) and the power consumption of the LED Driver. The power supply is designed to provide a maximum output power of 100W.

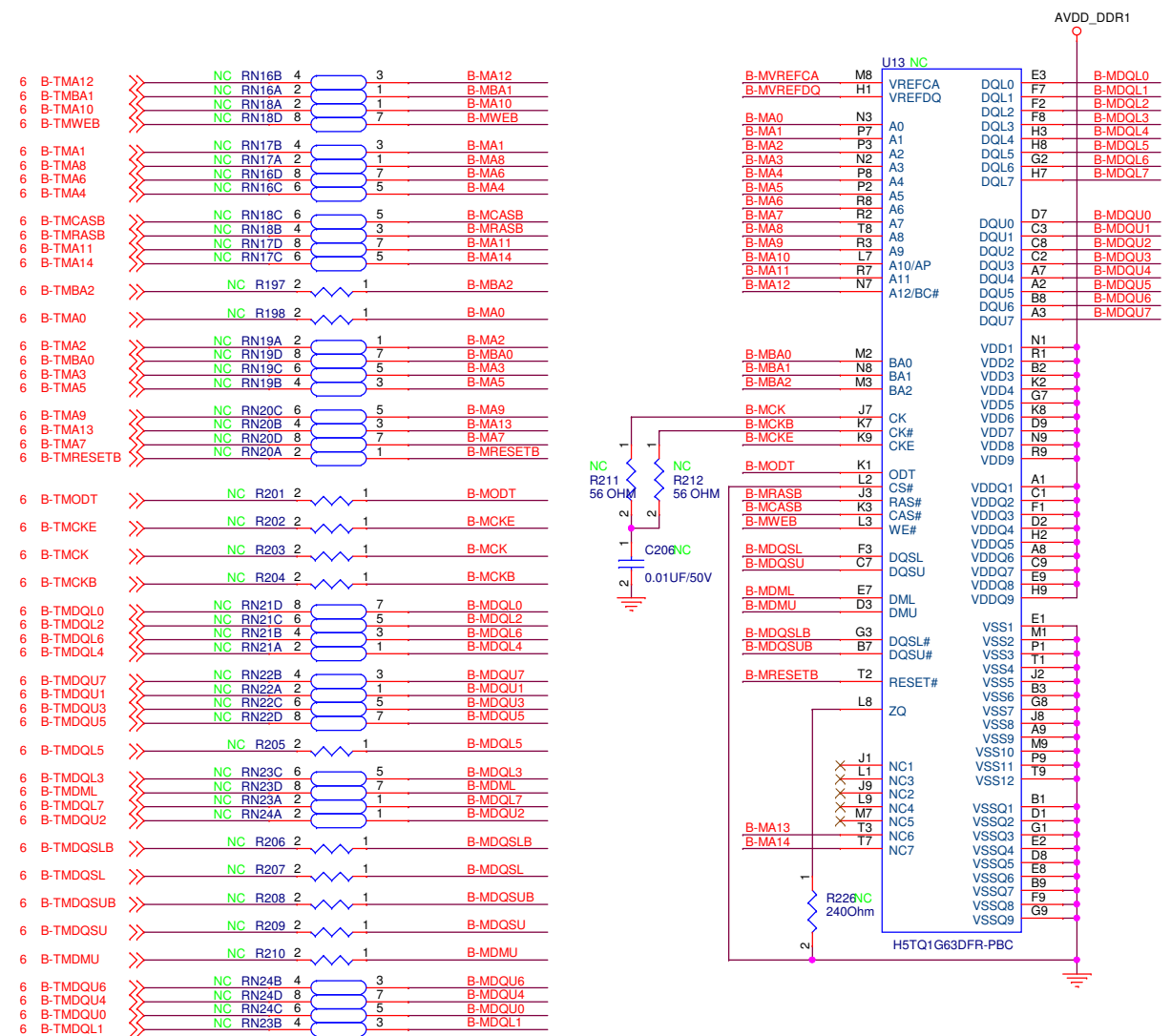
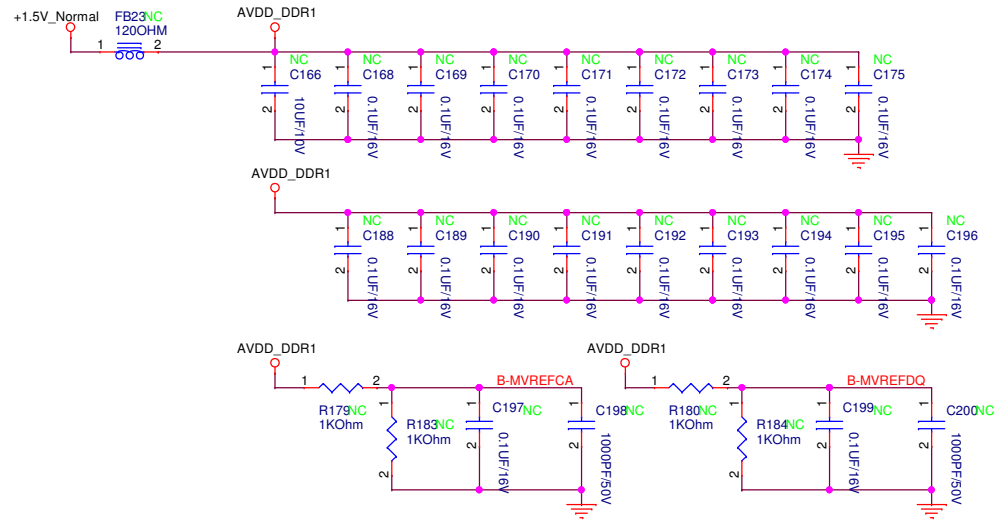


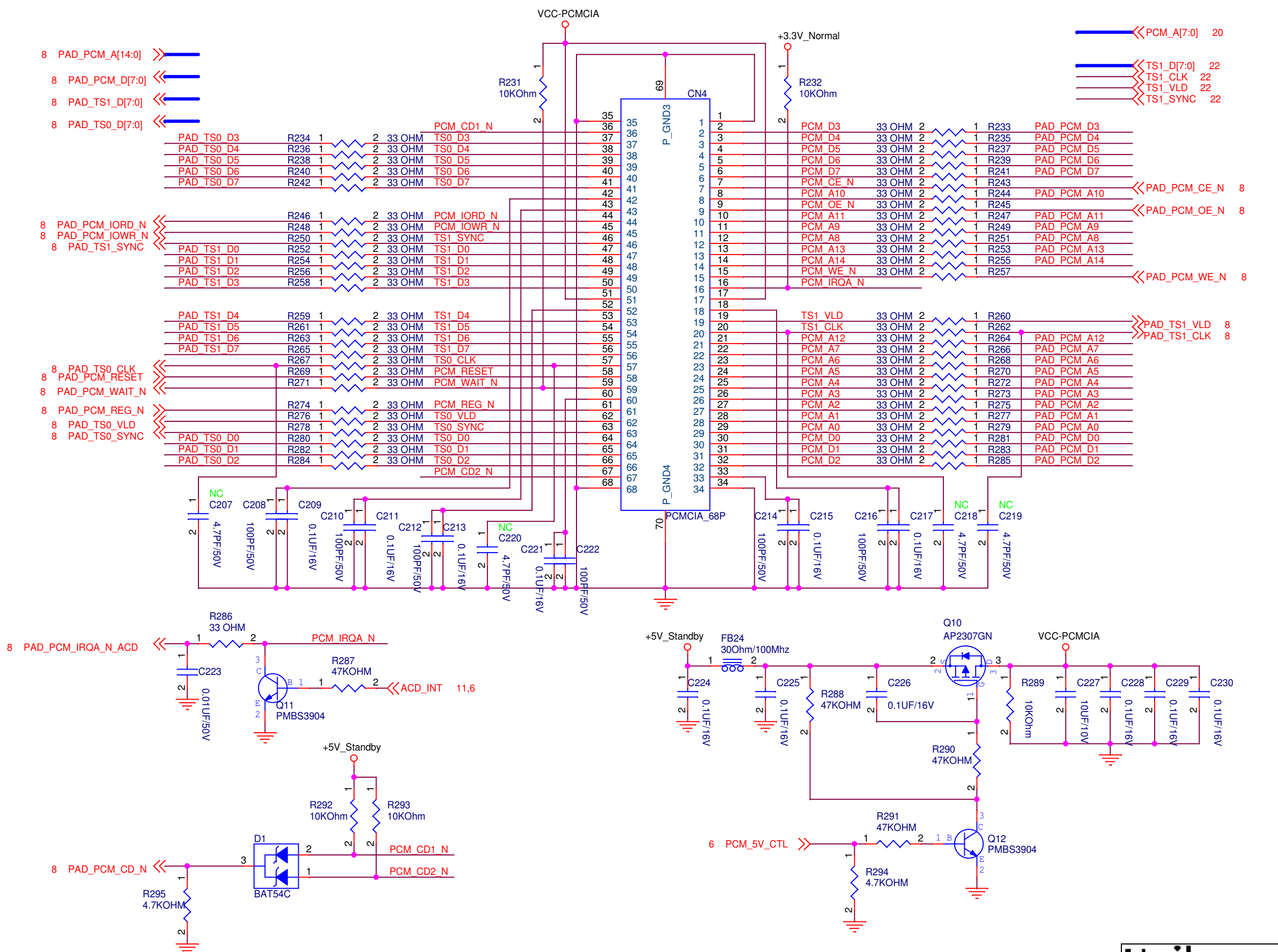


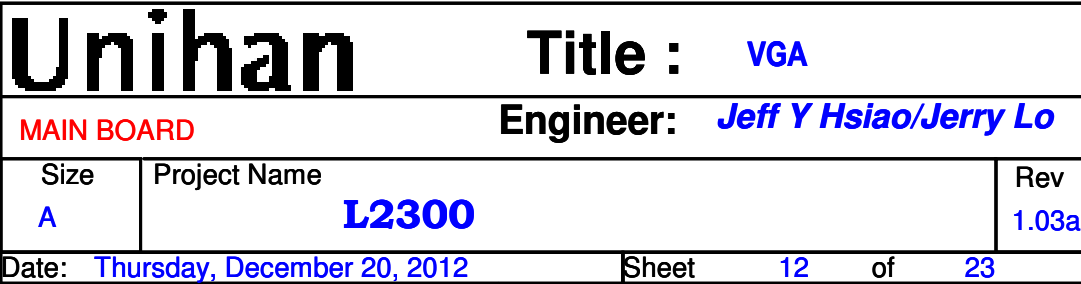


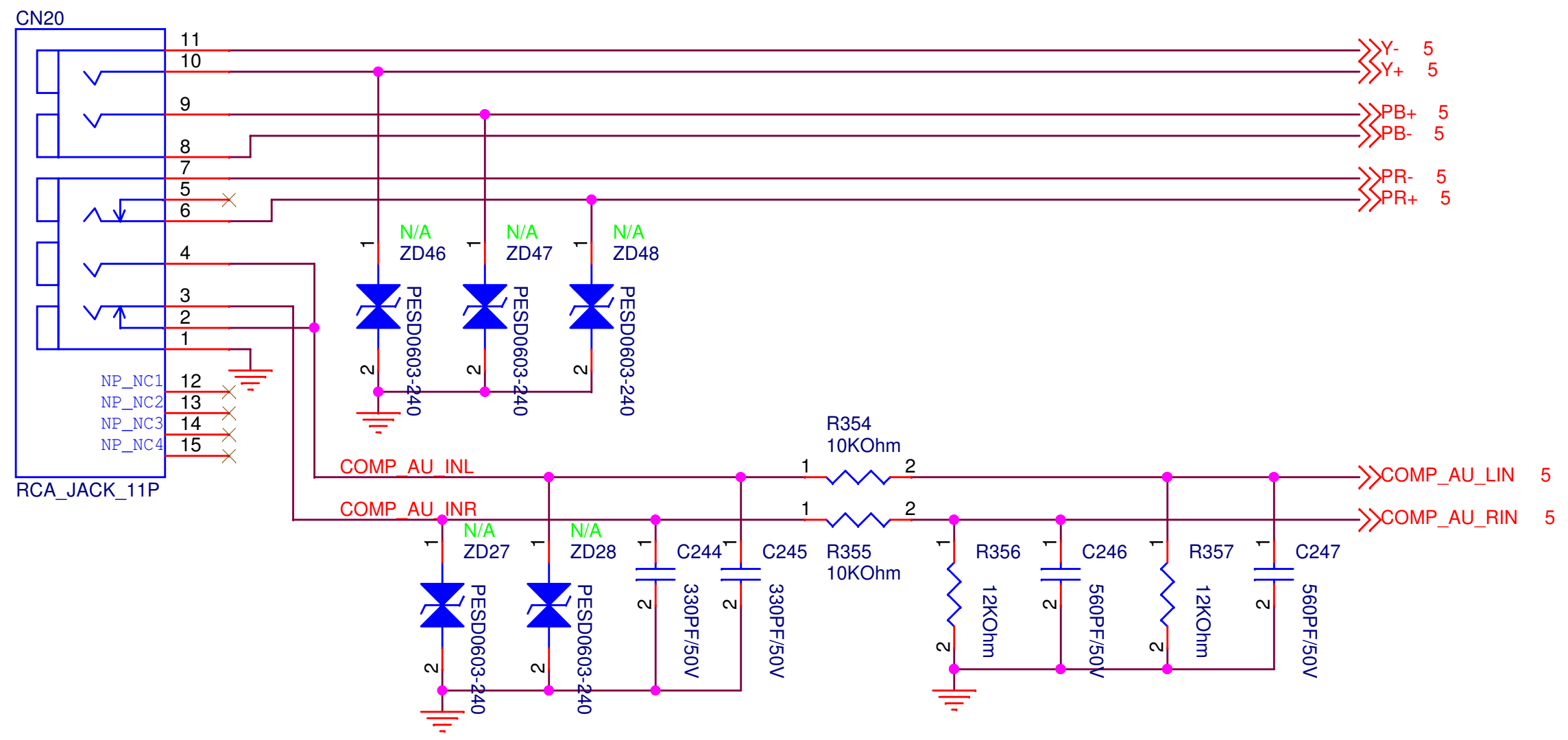


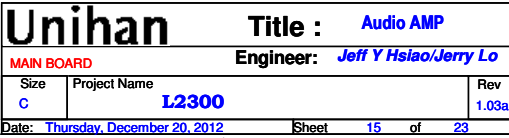


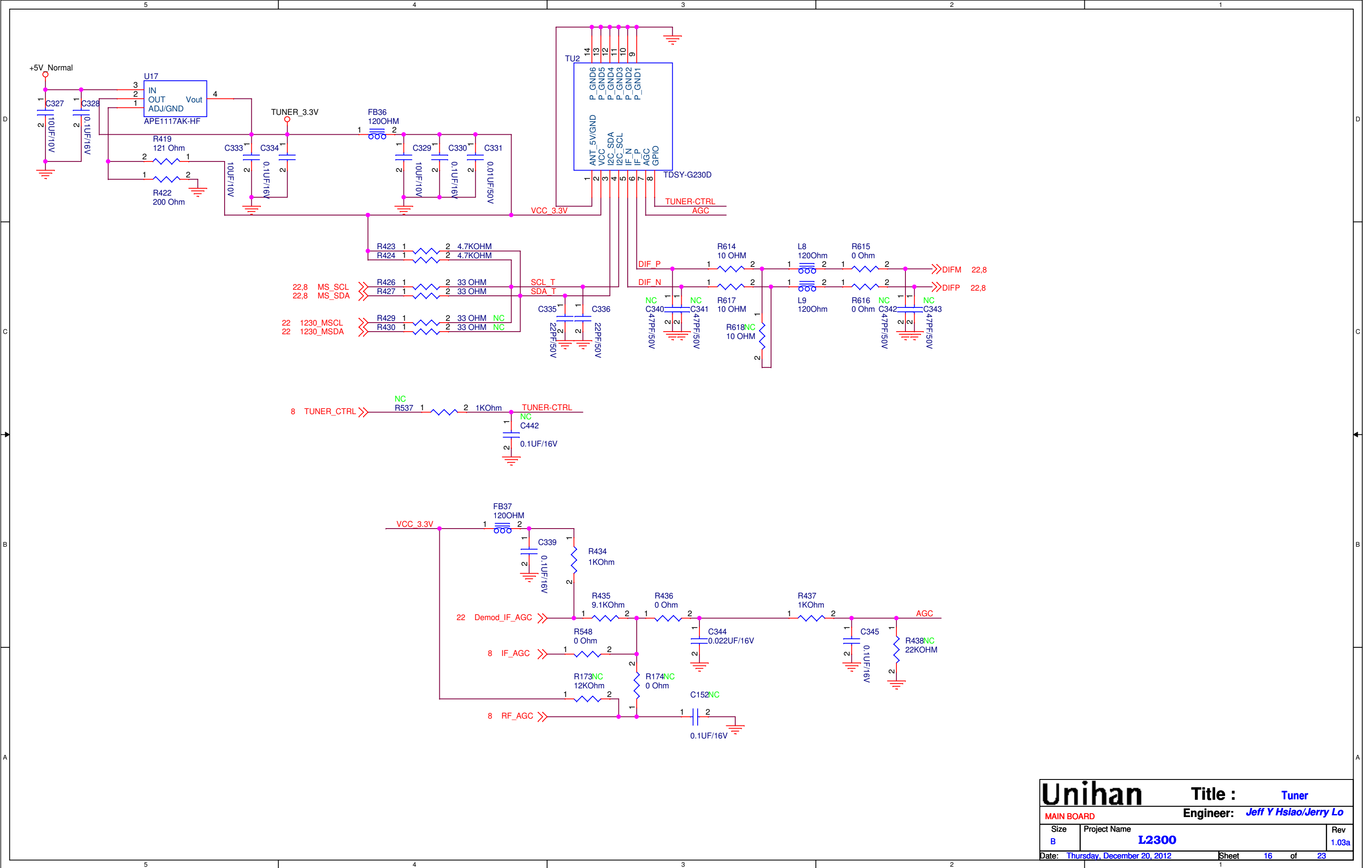


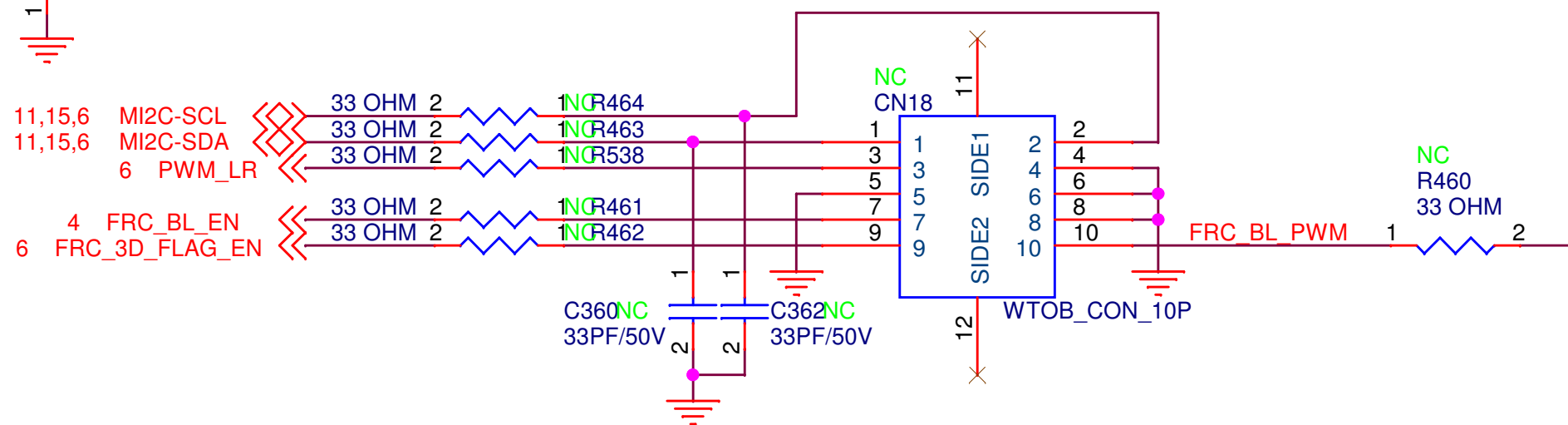
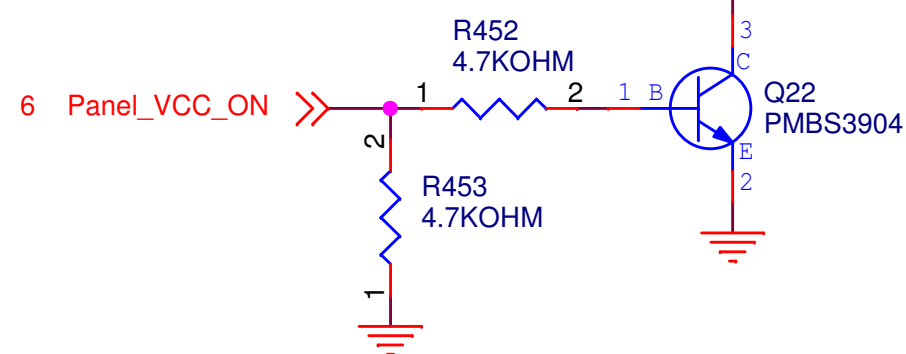
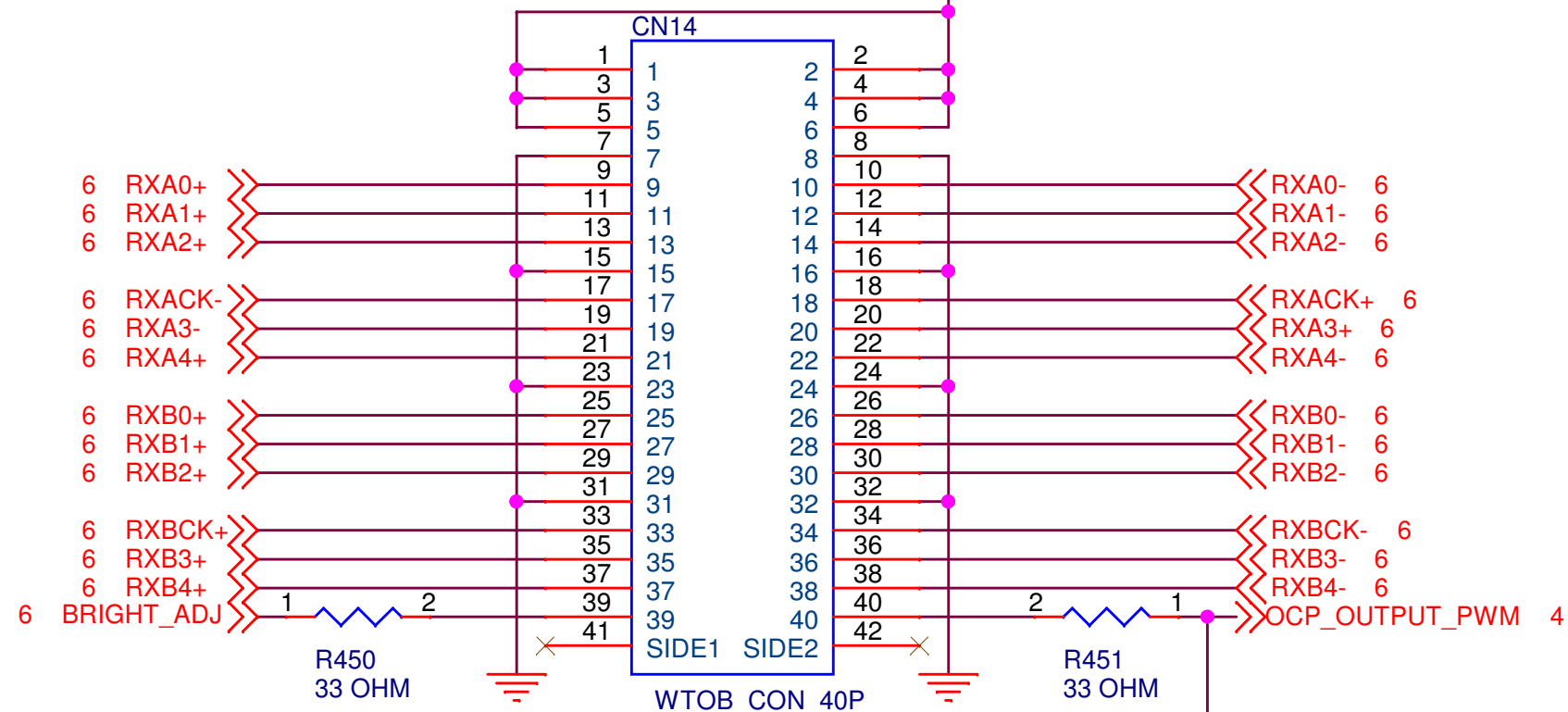
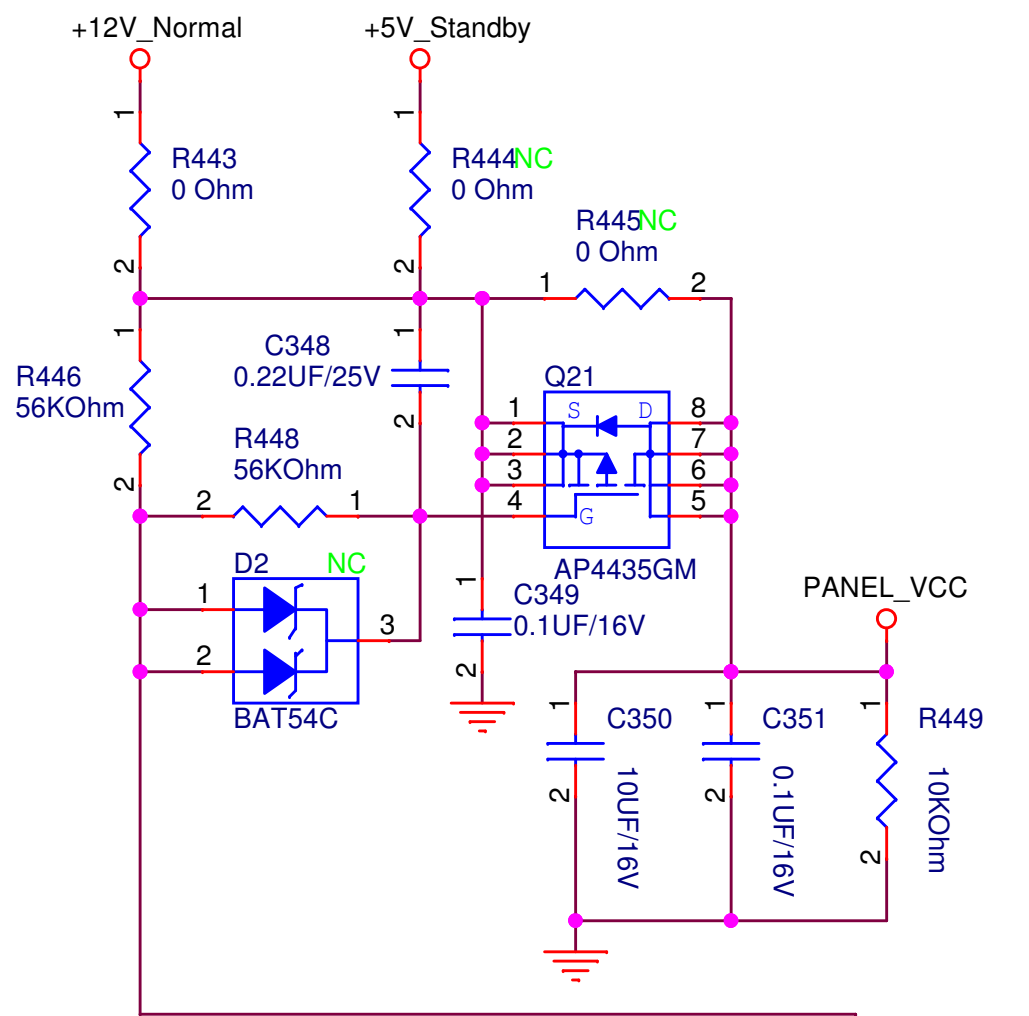




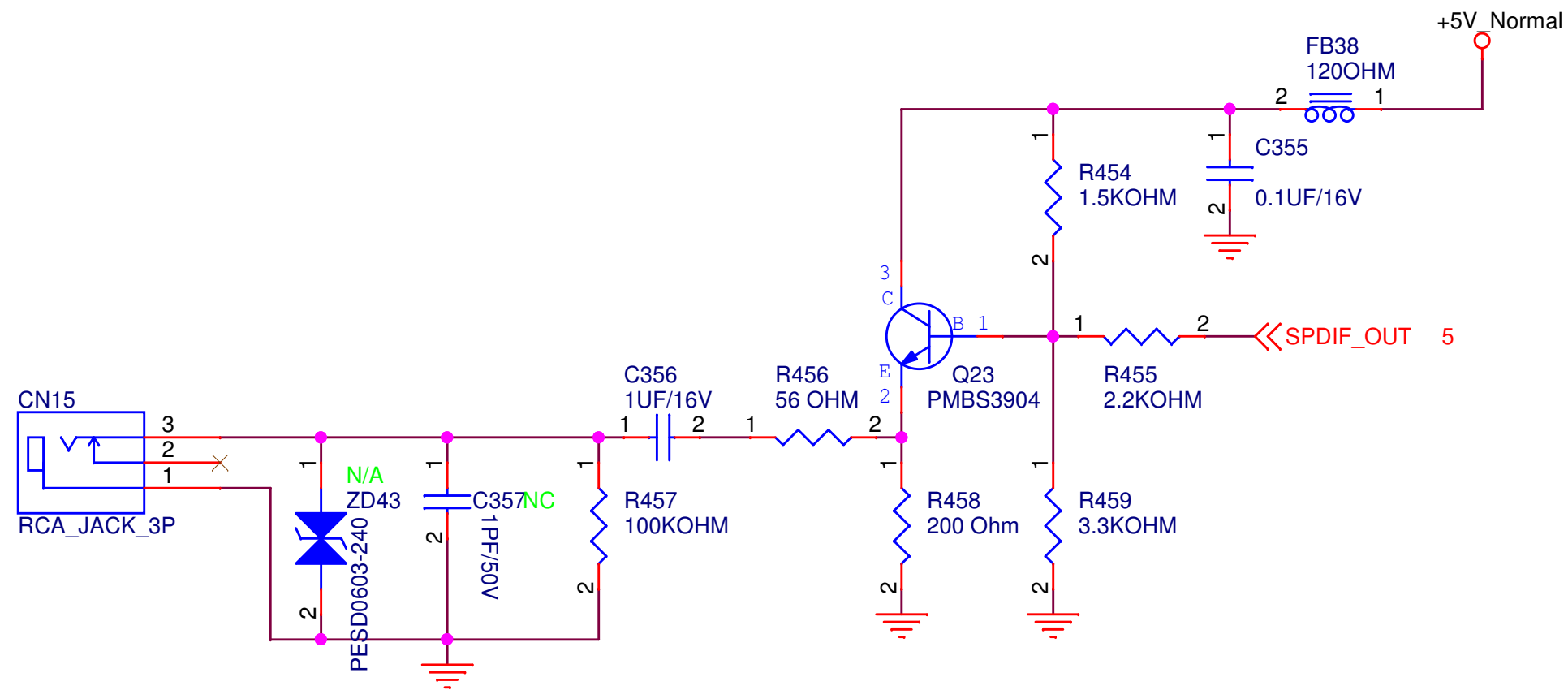




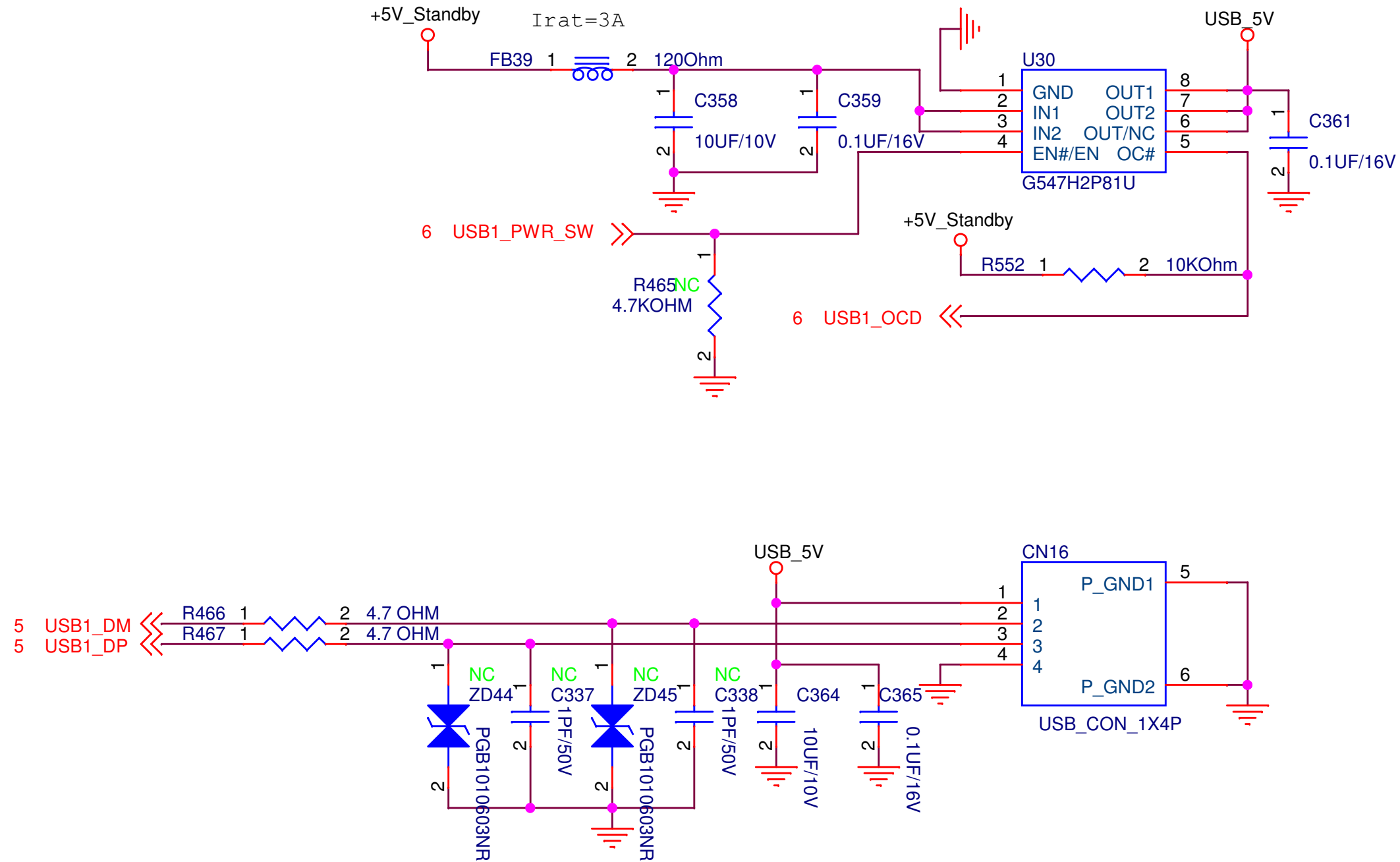




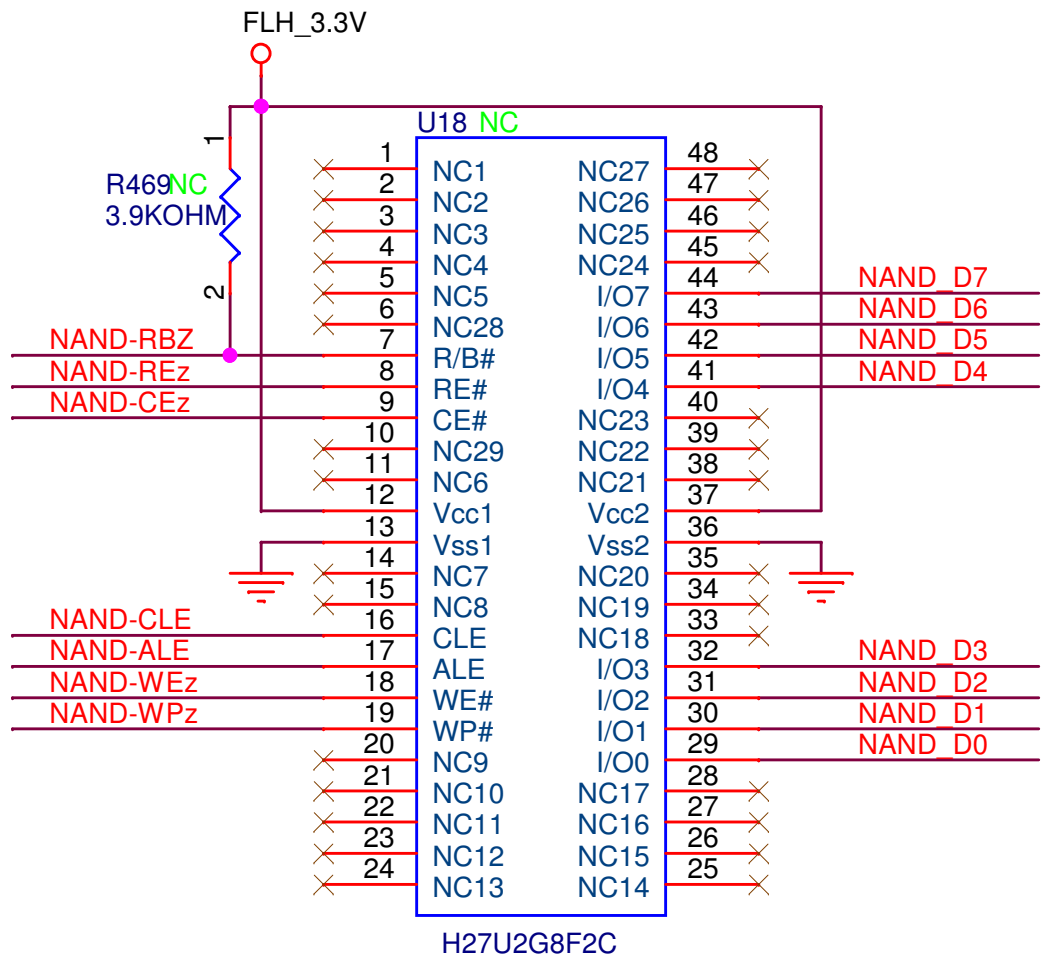
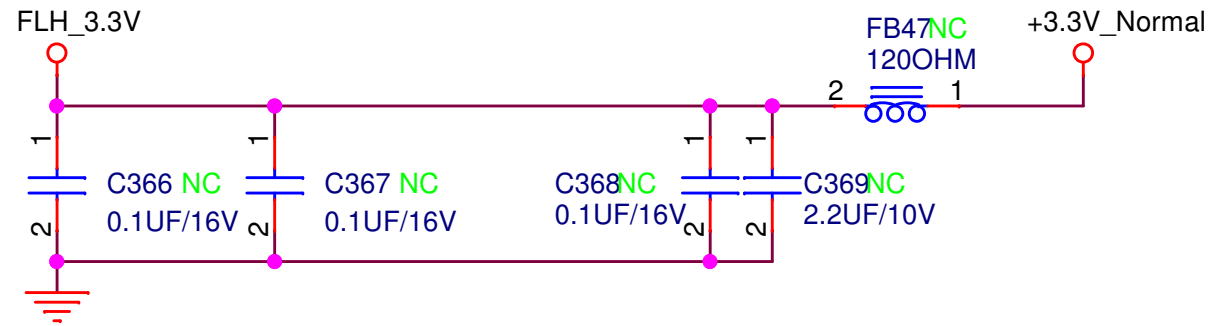
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MAIN BOARD		Engineer: Jeff Y Hsiao/Jerry Lo	
Size A	Project Name L2300		Rev 1.03a
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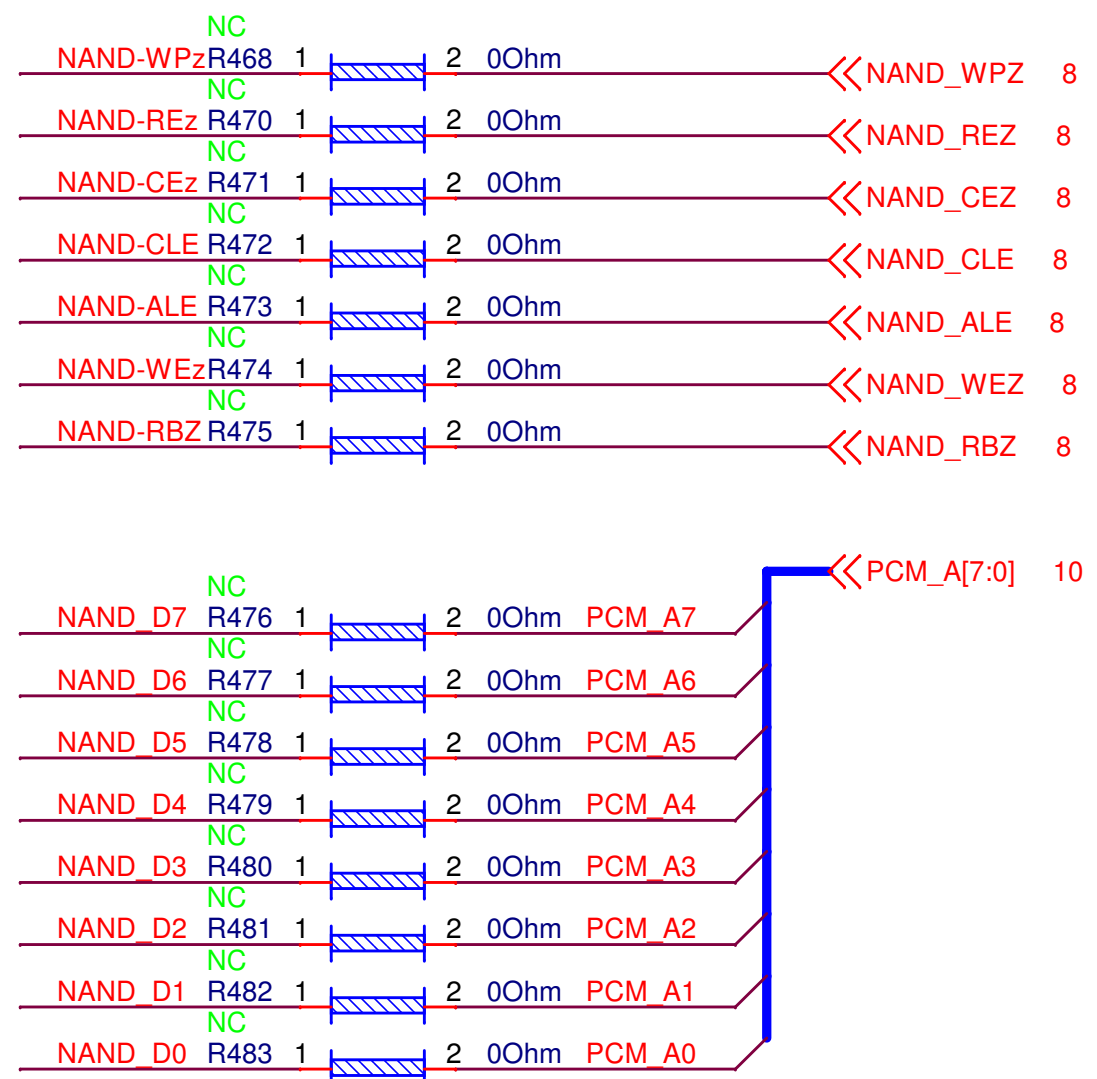
Unihan		Title : SPDIF OUT	
MAIN BOARD		Engineer: Jeff Y Hsiao/Jerry Lo	
Size A	Project Name L2300		Rev 1.03a
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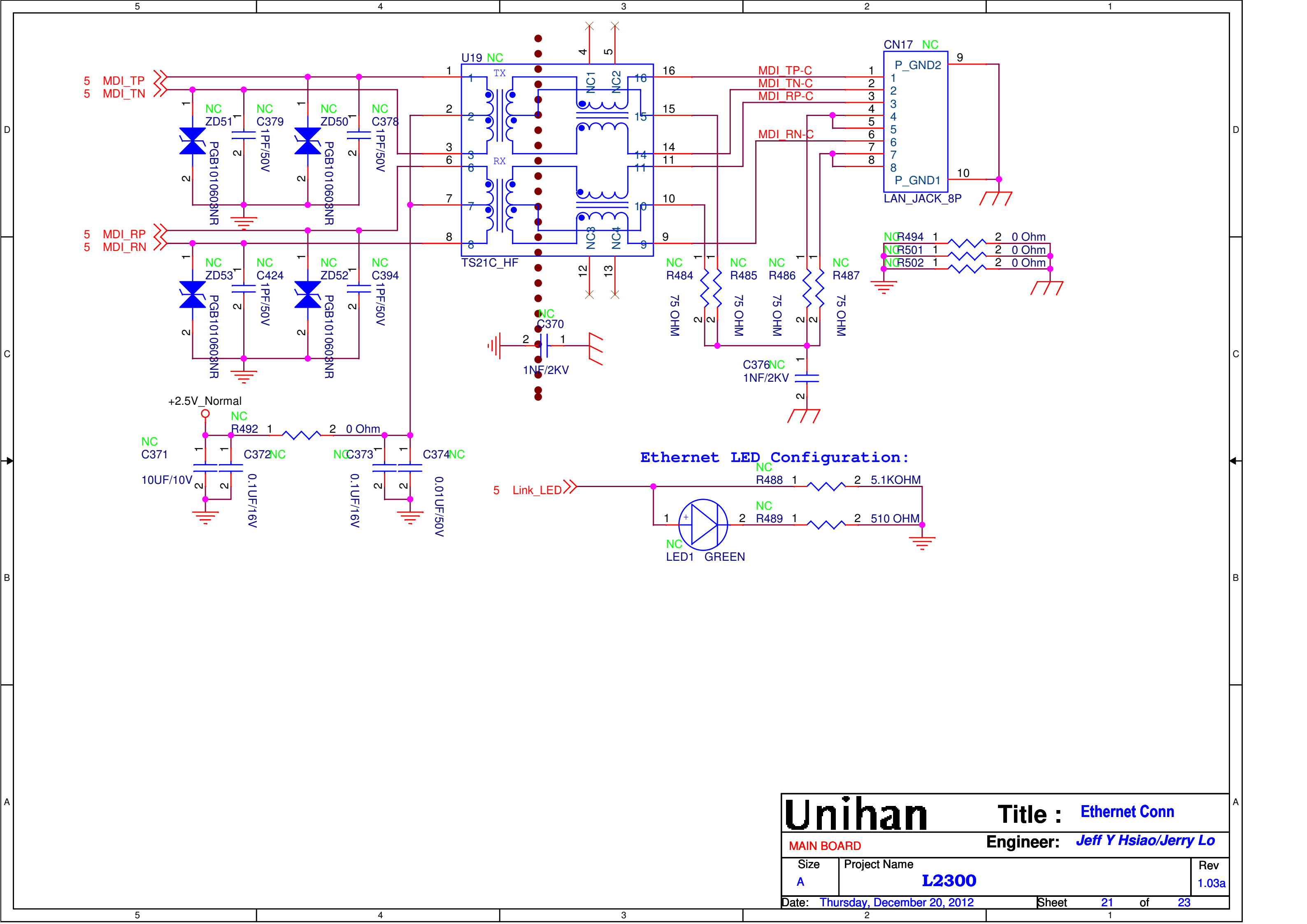


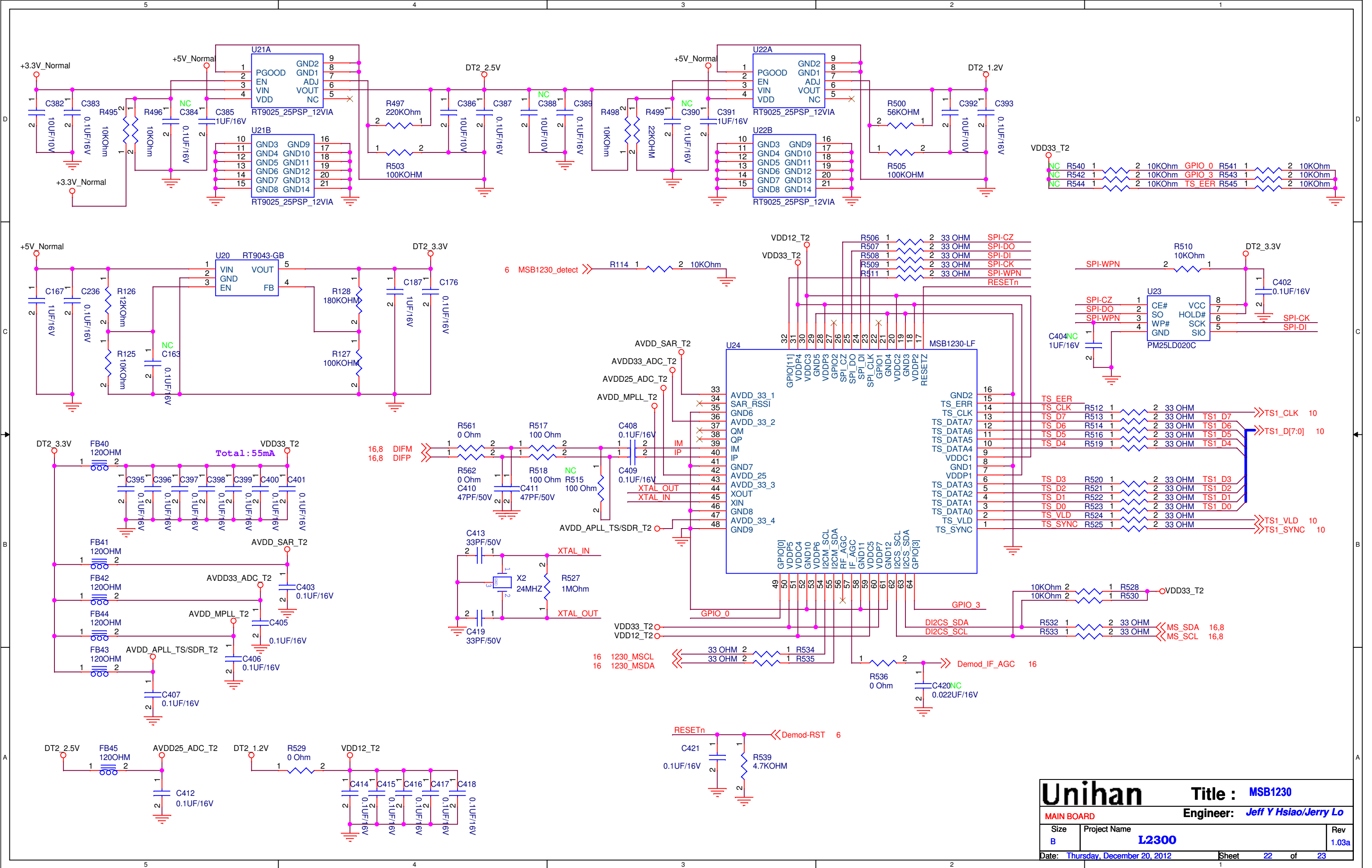
Unihan		Title : USB	
MAIN BOARD		Engineer: Jeff Y Hsiao/Jerry Lo	
Size A	Project Name L2300		Rev 1.03a
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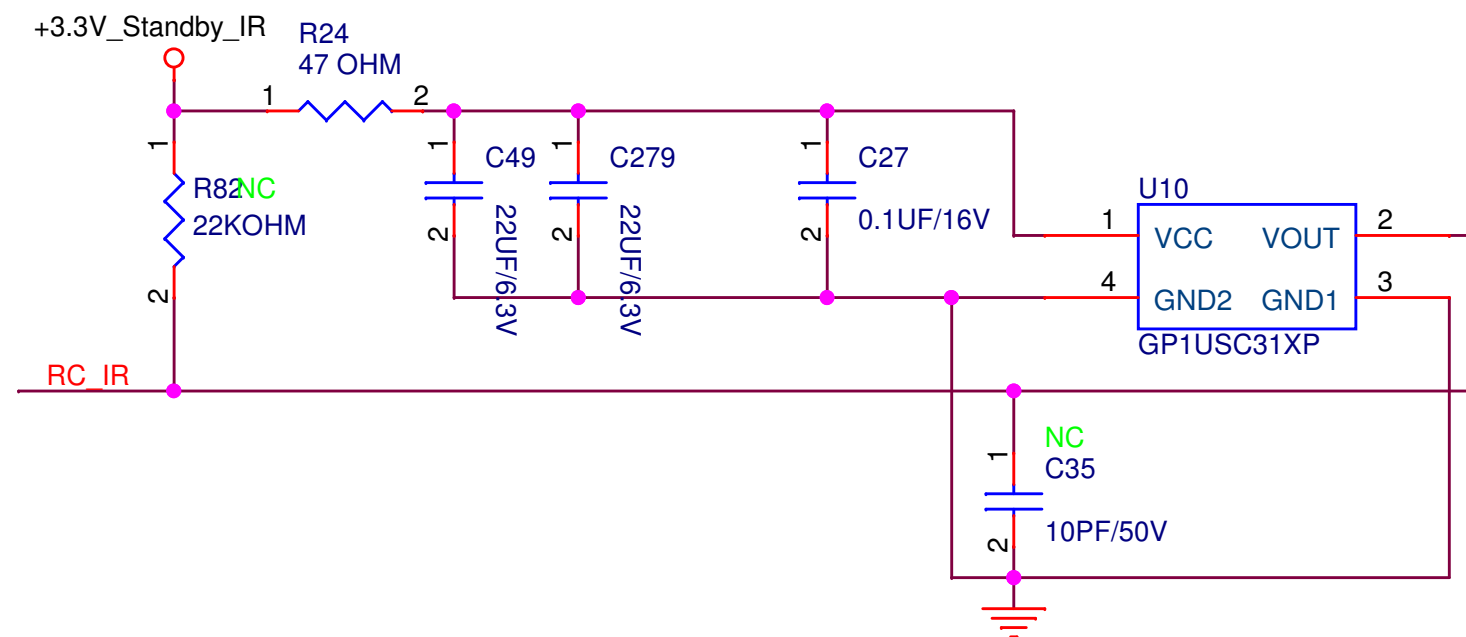
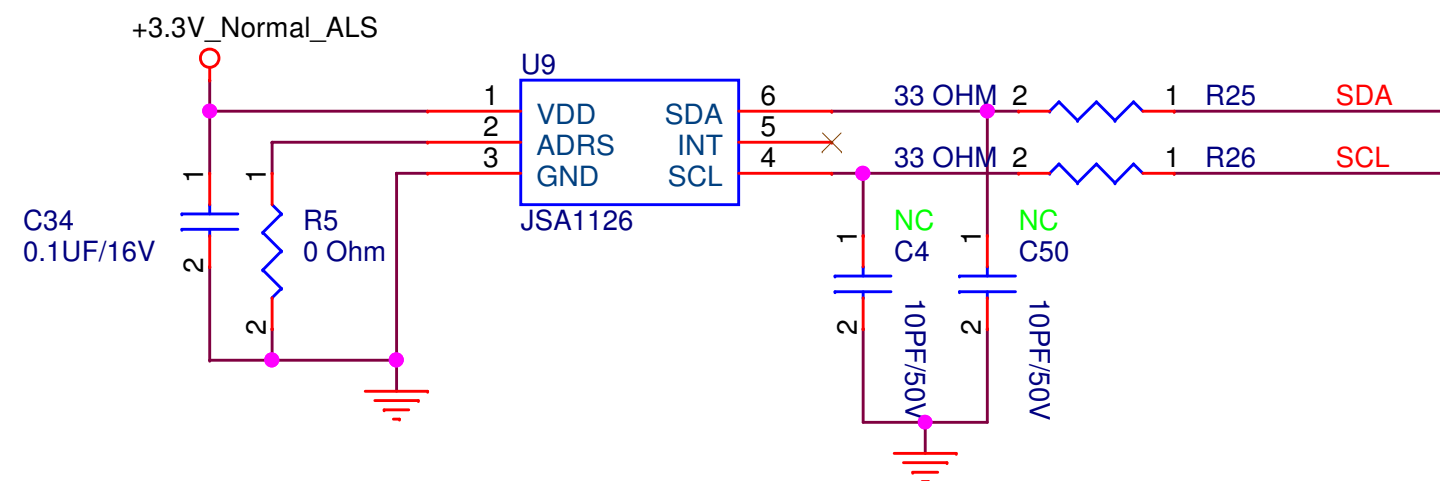
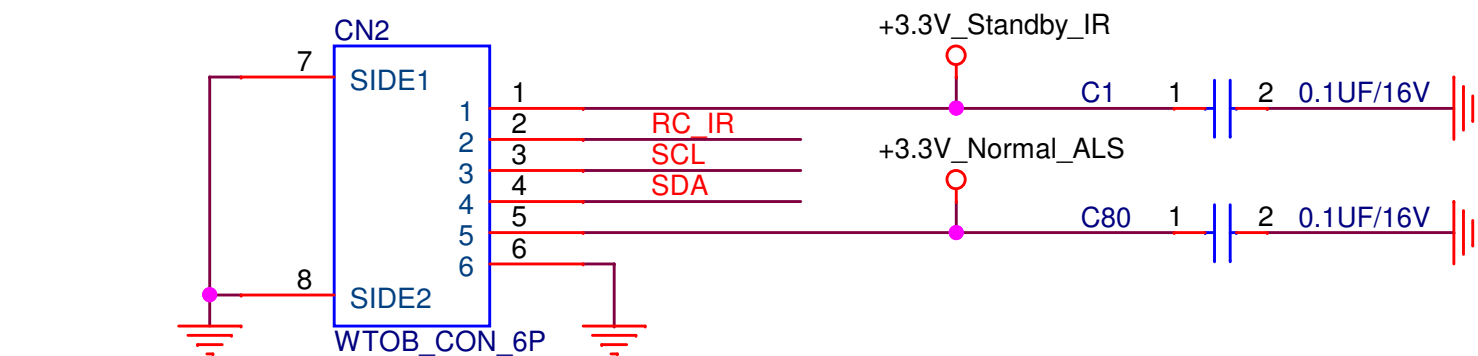


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UniHan		Title : IR_ALS	
MAIN BOARD		Engineer: Jeff Y Hsiao/Jerry Lo	
Size	Project Name		Rev
A	L2300		1.03a
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